Abstracts

A General Planar Circuit Simulator Based on Two-Dimensional TLM Method

P.P.M. So and W.J.R. Hoefer. "A General Planar Circuit Simulator Based on Two-Dimensional TLM Method." 1989 MTT-S International Microwave Symposium Digest 89.1 (1989 Vol. I [MWSYM]): 343-346.

A two-dimensional circuit simulator based on the TLM method (2D-TLM) has been developed. It can analyze two-dimensional circuits of arbitrary geometry containing both linear and nonlinear media. The circuit geometry is input graphically. Both time-domain and frequency-domain responses can be computed and visualized. As examples, a microstrip lowpass filter, a microstrip varactor multiplier, and a waveguide post-coupled filter have be analyzed and compared with other methods.

❑ Return to main document.